

**Supplemental
Notice of Allowability**

Application No.

10/799,046

Examiner

Kevin M. Nguyen

Applicant(s)

CHEN ET AL.

Art Unit

2629

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 03/11/2004.
2. ☒ The allowed claim(s) is/are 1-9.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some* c) ☐ None of the:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).
- * Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material

5. ☐ Notice of Informal Patent Application
6. ☐ Interview Summary (PTO-413),
Paper No./Mail Date _____
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____


RICHARD HJERPE
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600

SUPPLEMENTAL REASONS FOR ALLOWANCE

1. It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

2. Claims 1-9 are allowed.

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Kawabe et al (US 6,903,716) discloses gate lines G1, G2 are selected simultaneously within the video write-in period 302, images are written into the two pixel rows, then gate lines G3, G4 are selected while skipping the gate lines G1, G2 and images are written into the two pixel rows. The same images are written for each pixel column to the two pixel rows corresponding to the pair of gate lines and to the two pixel rows corresponding to the gate lines G3, G4 within the period in which the gate lines G1, G2 are selected (fig. 6, col. 18, lines 26-34). Wong (US 6,501,453) discloses "[t]he gate and the source of each switching transistor respectively couples to a second scanning signal and a video signal. One step of the driving method is shifting the video signal to have a dc voltage of a first predetermined voltage. Another step of the driving method is adding a second predetermined voltage to the pixel electrode after the second scanning signal changes the state of the switching transistor from turned-on to turn-off" (see the abstract). Ichikawa et al (US 5,028,916) discloses "[t]he row selector is connected to the address lines for sequentially selecting address lines through a scanning operation for image display. The column selector is connected to the data line for subjecting an incoming frame of image data to a time-division multiplexing and for

Art Unit: 2629

sequentially supplying block-segmented image data components to the data lines" (see the abstract).

4. The following is an examiner's statement of reasons for allowance:

The instant application is directed to a nonobvious improvement over the invention described in cited prior art. The improvement comprises a method of controlling a driver circuit of an LCD device, the method comprising the steps of: selecting a first gate driver from the at least two gate drivers based on a selection rule, wherein the first gate driver sequentially turns on a pre-determined range of gate lines starting from a gate line A and, thereby, a part of a picture image is displayed on the LCD device; and selecting a second gate driver from the at least two gate drivers based on the selection rule, wherein the second gate driver simultaneously turns on the pre-determined range of gate lines starting from a gate line B that is separated from the gate line A by a pre-determined gap of gate lines and, thereby, a part of a special image whose corresponding driving voltages are of a specific target value. This patentable distinction is included in the independent claim 1.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion


Any inquiry concerning this communication or earlier communications from the examiner should be directed to KEVIN M. NGUYEN whose telephone number is 571-272-7697. The examiner can normally be reached on MON-THU from 9:00-6:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, a supervisor RICHARD A. HJERPE can be reached on 571-272-7691. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8000.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the Patent Application Information Retrieval system, see <http://portal.uspto.gov/external/portal/pair>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

KMN
December 4, 2006

Kevin M. Nguyen
Patent Examiner
Art Unit 2629


RICHARD HJERPE
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600